

CS 370 Class Schedule for Spring 2020

January 22: Lecture 1 (Class Guidelines & Chapter1 “Digital Systems and Information”)

January 27: Lecture 2 (Chapter 2 “Combinational Logic Circuits”, part 1 – Gate Circuits and Boolean Equations)

January 29: Lecture 3 (Chapter 2 “Combinational Logic Circuits”, part 1 – Standard Forms)

February 3: Lecture 4 (Chapter 2 “Combinational Logic Circuits”, part 2 – Circuit Optimization)

February 5: Lecture 5 (Chapter 2 “Combinational Logic Circuits”, part 2 – K-Map Manipulation)

February 10: Lecture 6 (Chapter 2 “Combinational Logic Circuits”, part 3 – Additional Gates and Circuits)

February 12: Lecture 7 (Chapter 3 “Combinational Logic Design”, part 1 – Implementation Technology and Logic Design)

February 17: Lecture 8 (Chapter 3 “Combinational Logic Design”, part 2 – Functions and Functional Blocks)

February 19: Lecture 9 (Preview for Midterm Exam One)

February 24: Midterm Exam One (75 minutes in class)

February 26: Lecture 10 (Error Detection and Correction for Lab Assignment 2)

March 2: Lecture 11 (Chapter 3 “Combinational Logic Design”, part 3 – Arithmetic Functions and Circuits)

March 4: Lecture 12 (Chapter 3 “Combinational Logic Design”, part 3 – Binary Subtraction and Adder-Subtractors)

March 9: Introduction to Pipelining (An important topic of computer architecture)

March 11: Lecture 13 (Chapter 4 “Sequential Circuits”, Part 1 – Storage Element Latches)

March 16: Lecture 14 (Chapter 4 “Sequential Circuits”, Part 1 – Flip-Flops and Sequential Circuit Analysis)

March 18: Lecture 15 (Chapter 4 “Sequential Circuits”, Part 2 – Sequential Circuit Design)

March 23: Lecture 16 (Chapter 4 “Sequential Circuits”, Part 2 – Sequential Circuit Design: Two more examples)

March 25: Lecture 17 (Preview for Midterm Exam Two)

April 6 Midterm Exam Two (75 minutes in class)

April 8: Lecture 18 (Chapter 5 “Digital Hardware Implementation”, Programmable Implementation Technologies and Introduction of Lab Assignment3)

April 13: Summaries of midterm exam one & midterm exam two

April 15: Lecture 19 (Chapter 6 “Registers and Register Transfers”, Part 1 – Registers, Microoperations and Implementations)

April 20: Lecture 20 (Chapter 6 “Registers and Register Transfers”, Part 2 – Counters and Register Cell Design)

April 22: Lecture 21 (Chapter 7 “Memory Basics”)

April 27: Lecture 22 (Chapter 8 “Computer Design Basics”, Datapath)

April 29: Lecture 23 (Warm-up exercises; Tao will show you how to solve some sample questions from chapters that haven’t been covered by the two midterm exams)

May 4: [Lecture 24 \(Preview for Final Exam\)](#)

Final Exam

For Section One: 13:00 – 15:00 on May 11 at P-144

For Section Two: 15:30 – 17:30 on May 11 at GMCS-310