

CS 370-Section 1 Class Schedule for Fall 2017

(Class meeting time: MW 16:00~17:15; Location: Music-120)

August 28: Lecture 1 (Class Guidelines & Chapter1 “Digital Systems and Information”)

August 30: Lecture 2 (Chapter 2 “Combinational Logic Circuits”, part 1 – Gate Circuits and Boolean Equations)

September 6: Lecture 3 (Chapter 2 “Combinational Logic Circuits”, part 1 – Standard Forms)

September 11: Lecture 4 (Chapter 2 “Combinational Logic Circuits”, part 2 – Circuit Optimization)

September 13: Lecture 5 (Chapter 2 “Combinational Logic Circuits”, part 2 – K-Map Manipulation)

September 18: Lecture 6 (Chapter 2 “Combinational Logic Circuits”, part 3 – Additional Gates and Circuits)

September 20: Lecture 7 (Chapter 3 “Combinational Logic Design”, part 1 – Implementation Technology and Logic Design)

September 25: Lecture 8 (Chapter 3 “Combinational Logic Design”, part 2 – Functions and Functional Blocks)

September 27: [Lecture 9 \(Preview for Midterm Exam One\)](#)

October 2: Midterm Exam One (75 minutes in class)

October 4: Lecture 10 (Error Detection and Correction for Lab Assignment 2)

[October 9: Summary of Midterm Exam One](#)

October 11: Lecture 11 (Chapter 3 “Combinational Logic Design”, part 3 – Arithmetic Functions and Circuits)

October 16: Lecture 12 (Chapter 3 “Combinational Logic Design”, part 3 – Binary Subtraction and Adder-Subtractors)

October 18: Lecture 13 (Chapter 4 “Sequential Circuits”, Part 1 – Storage Element Latches)

October 23: Lecture 14 (Chapter 4 “Sequential Circuits”, Part 1 – Flip-Flops and Sequential Circuit Analysis)

October 25: Lecture 15 (Chapter 4 “Sequential Circuits”, Part 2 – Sequential Circuit Design)

October 30: Lecture 16 (Chapter 4 “Sequential Circuits”, Part 2 – Sequential Circuit Design: Two more examples)

[November 1: Lecture 17 \(Preview for Midterm Exam Two\)](#)

November 6: Midterm Exam Two (75 minutes in class)

November 8: Lecture 18 (Chapter 5 “Digital Hardware Implementation”, Programmable Implementation Technologies and Introduction of Lab Assignment3)

[November 13: Summary of Midterm Exam Two](#)

November 15: Lecture 19 (Chapter 6 “Registers and Register Transfers”, Part 1 – Registers, Microoperations and Implementations)

November 20: Lecture 20 (Chapter 6 “Registers and Register Transfers”, Part 2 – Counters and Register Cell Design)

November 27: Lecture 21 (Chapter 7 “Memory Basics”)

November 29: Lecture 22 (Chapter 8 “Computer Design Basics”, Datapath)

December 4: [Lecture 23 \(Preview for Final Exam\)](#)

December 18: **Final Exam (2 hours: 15:30 ~ 17:30)**